

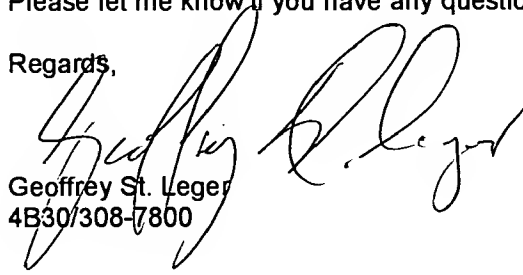
July 3, 2001

Dear Mr. Bataille,

Attached please find the results of your search request for application #09/124,649. To briefly summarize what's contained herein, I found several patents that mention the ability of the user to configure a logic chip for single or dual ports. The NPL literature mainly mentioned product releases by companies like Xilinx or Altera, many of which mentioned support for either single or dual ports. Along with checking the foreign patent files and Dialog's scientific and fulltext general files, I also checked IBM's Technical Disclosure Bulletins but found nothing there.

Please let me know if you have any questions.

Regards,



Geoffrey St. Leger  
4B30/308-7800